

<p>U.S. Dept. of Commerce Patent &amp; Trademark Office</p> <p><b>List of Documents</b> Cited by Applicant (Use several sheets if necessary)</p> <p><b>JAN 22 2001</b></p>	<p>Atty. Docket <b>TD-152</b> <span style="float: right;">SN 09/591,532</span></p> <p>Applicant: Baldwin</p> <p>Filing Date: 06/09/2000</p>
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**U.S. PATENT DOCUMENTS**

Ex'r's In'l	Document Number	Date	Name	Class	Sub-class	Filing Date, if applicable
FF	A1	5,548,709	8/20/96	Hannah <i>et al.</i>		
	B1	5,611,064	3/11/97	Maund <i>et al.</i>		
	C1	5,706,481	1/6/98	Hannah <i>et al.</i>		
	D1	5,828,382	10/27/98	Wilde		
	E1	5,831,640	11/3/98	Wang <i>et al.</i>		
	F1	5,842,015	11/24/98	Cunniff <i>et al.</i>		
	G1	5,880,737	3/9/99	Griffin <i>et al.</i>		
	H1	5,886,706	3/23/99	Alcorn <i>et al.</i>		
	J1	5,999,189	12/7/99	Kajiya <i>et al.</i>		
	K1	6,002,407	12/14/99	Fadden		
	L1	6,002,410	12/14/99	Battle		
V	M1	6,011,565	1/4/00	Kuo <i>et al.</i>		

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

FF	N1	Cox et al., "Multi-Level Texture Caching for 3D Graphics Hardware," <i>Proceedings of the 25th International Symposium on Computer Architecture</i> , 1998
FF	O1	Foley et al., <i>Computer Graphics: Principles and Practice</i> (2.ed. 1990, corr.1995), pages 741-744
FF	P1	Hakura and Gupta, "The Design and Analysis of a Cache Architecture for Texture Mapping," <i>Proceedings of the 24th International Symposium on Computer Architecture</i> , 1997
FF	Q1	Paul S. Heckbert, "Fundamentals of Texture Mapping and Image Warping," Thesis submitted to Dept. of EE and Computer Science, University of California, Berkeley, 6/17/94 <span style="float: right;">6/17/1989</span>
FF	R1	Heckbert, "Survey of <del>Computer Graphics</del> <sup>Texture Mapping</sup> ," <i>IEEE Computer Graphics</i> , November 1986, pp.56
FF	S1	Igehy et al., "Prefetching in a Texture Cache Architecture", <i>IEEE</i>
FF	T1	Blinn, Jim Blinn's Corner: "Dirty Pixels", <i>IEEE Computer Graphics and Applications Journal</i> , Jan. 1989, vol. 9, issue 4

Form 100-149 U.S. Dept. of Commerce Patent & Trademark Office		Atty. Docket TD-152 SN 09/591,532
List of Documents Cited by Applicant (Use several sheets if necessary)		Applicant: Baldwin Filing Date: 06/09/2000 Group:
JAN 22 2001 PATENT & TRADEMARK OFFICE	Blinn, Jim Blinn's Corner: "A Trip Down the Graphics Pipeline: Line Clipping", <i>IEEE Computer Graphics and Applications Journal</i> , Jan. 1991, vol. 11, issue 1	RECEIVED JAN 24 2001 Technology Center 2000
V1	Blinn, Jim Blinn's Corner: "A Trip Down the Graphics Pipeline: Pixel Coordinates", <i>IEEE Computer Graphics and Applications Journal</i> , Jul. 1991, vol. 11, issue 4	
W1	Blinn, Jim Blinn's Corner: "A Trip Down the Graphics Pipeline: Sub-Pixelic Particles" <i>IEEE Computer Graphics and Applications Journal</i> , Sep 1991, vol. 11, issue 5	
X1	Blinn, Jim Blinn's Corner: "A Trip Down the Graphics Pipeline: Grandpa, What Dopes Viewport Mean?", <i>IEEE Computer Graphics and Applications Journal</i> , Jan. 1992, vol. 12, iss. 1	
Examiner: <i>Forensic Forensic</i>		Date Considered: 7/16/03
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		

IDS (paper #4) is a reference which was cited in IDS for the application as reference "01", but was not included at the time of filing and therefore was sent in separately.